APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS

FIG. 1

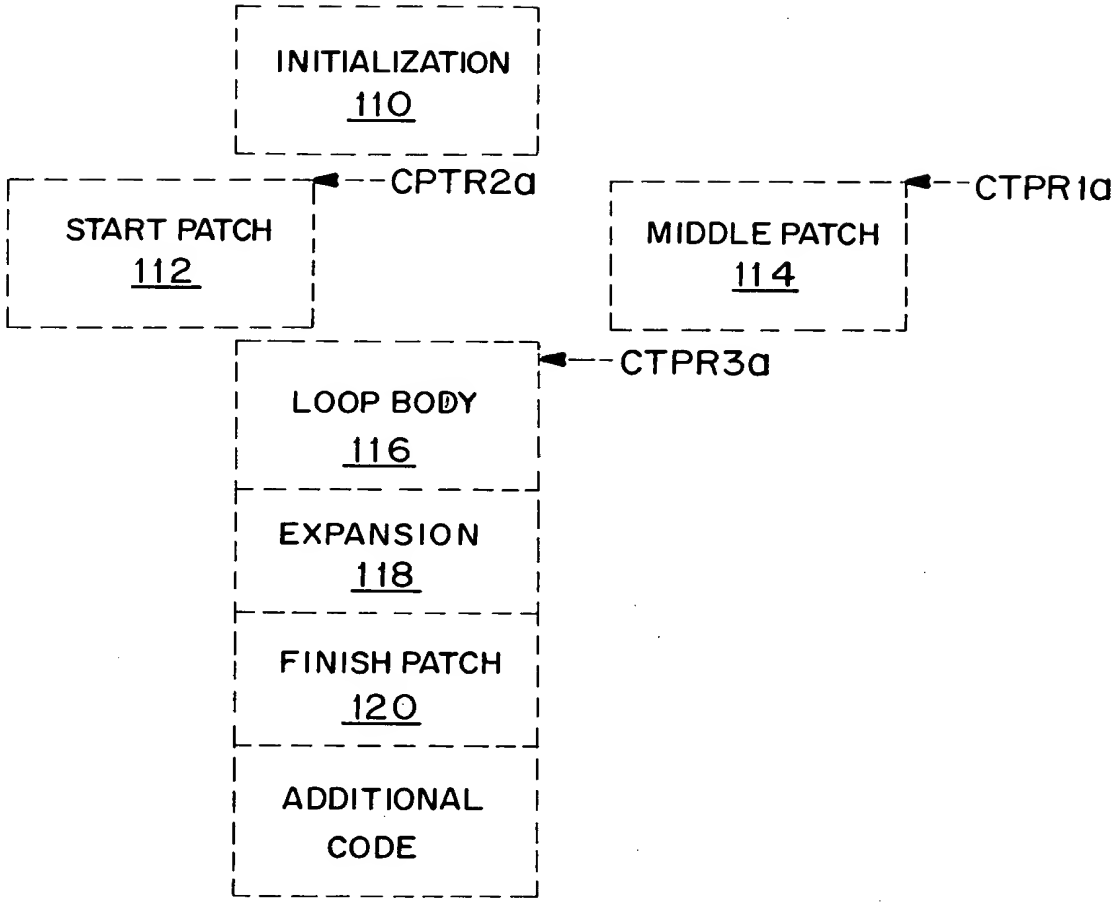

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FIG. 2

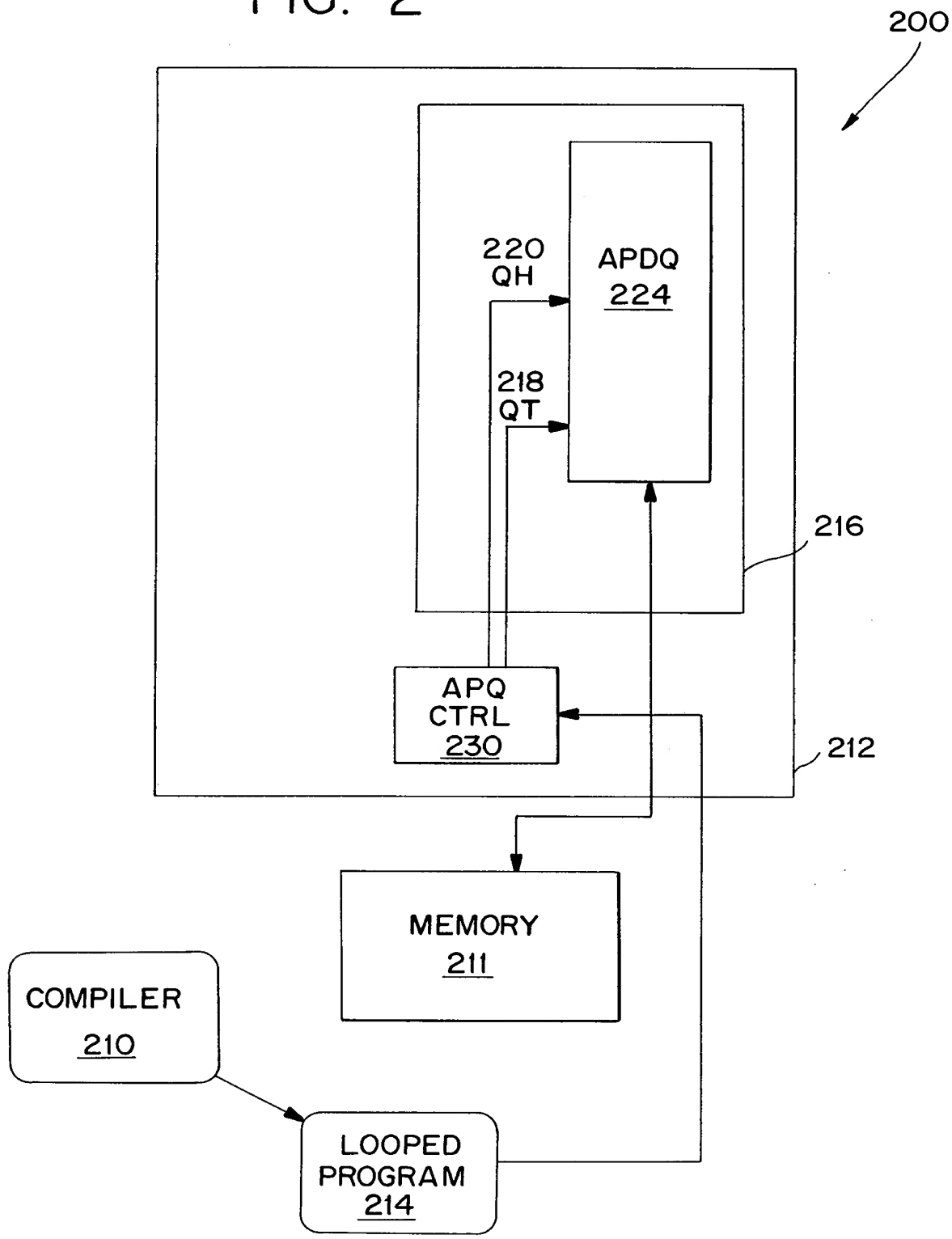
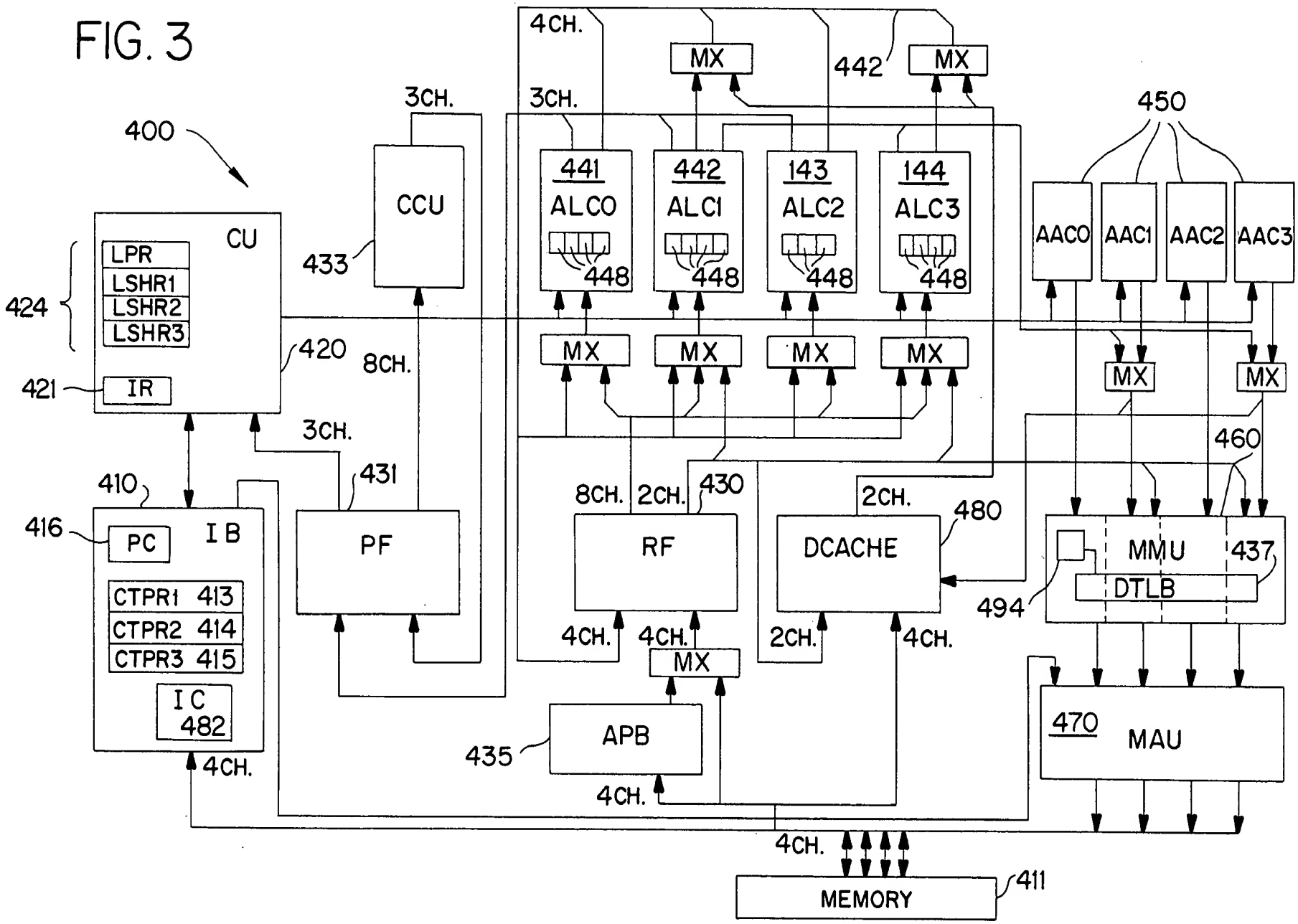


FIG. 3



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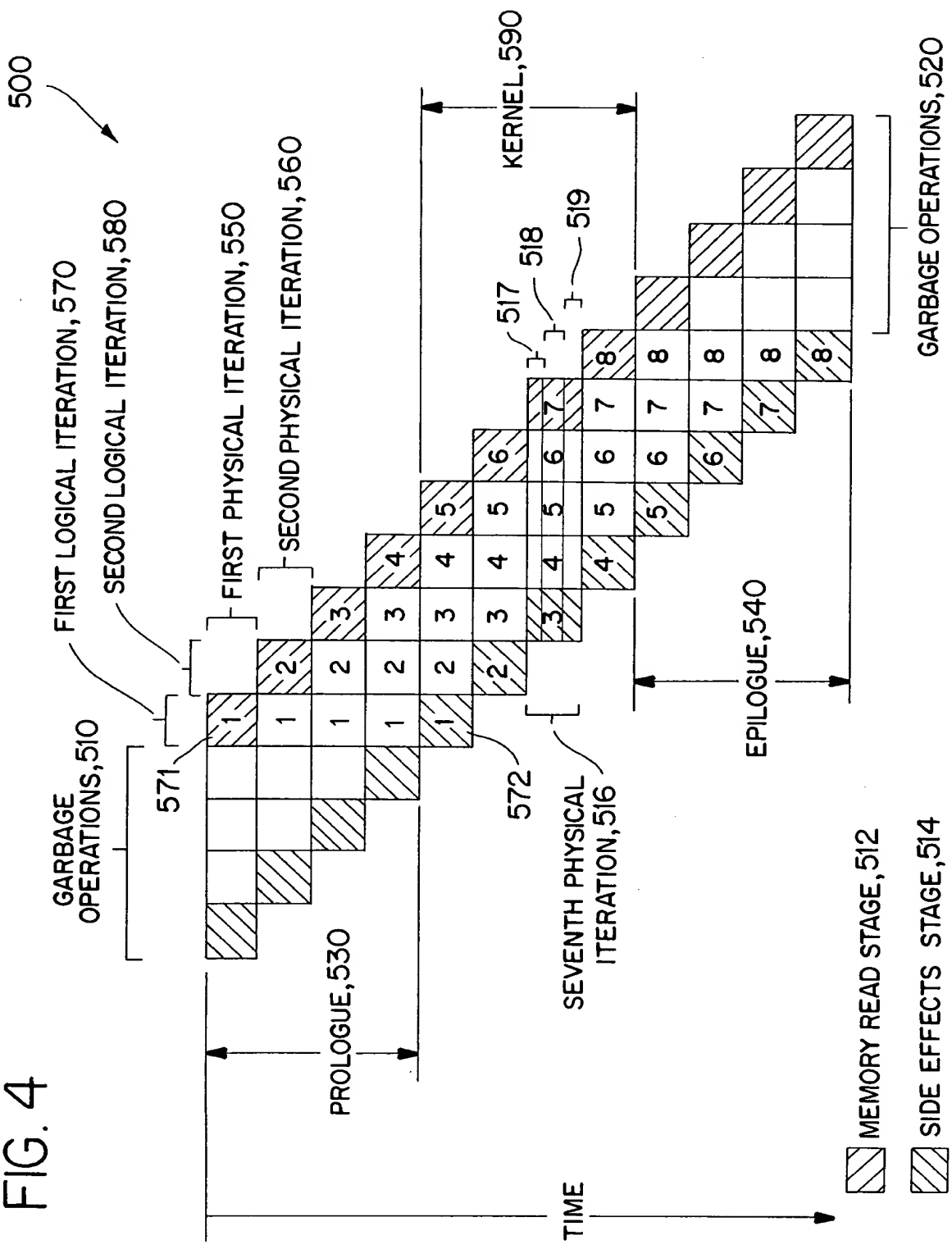

FIG. 4

APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS

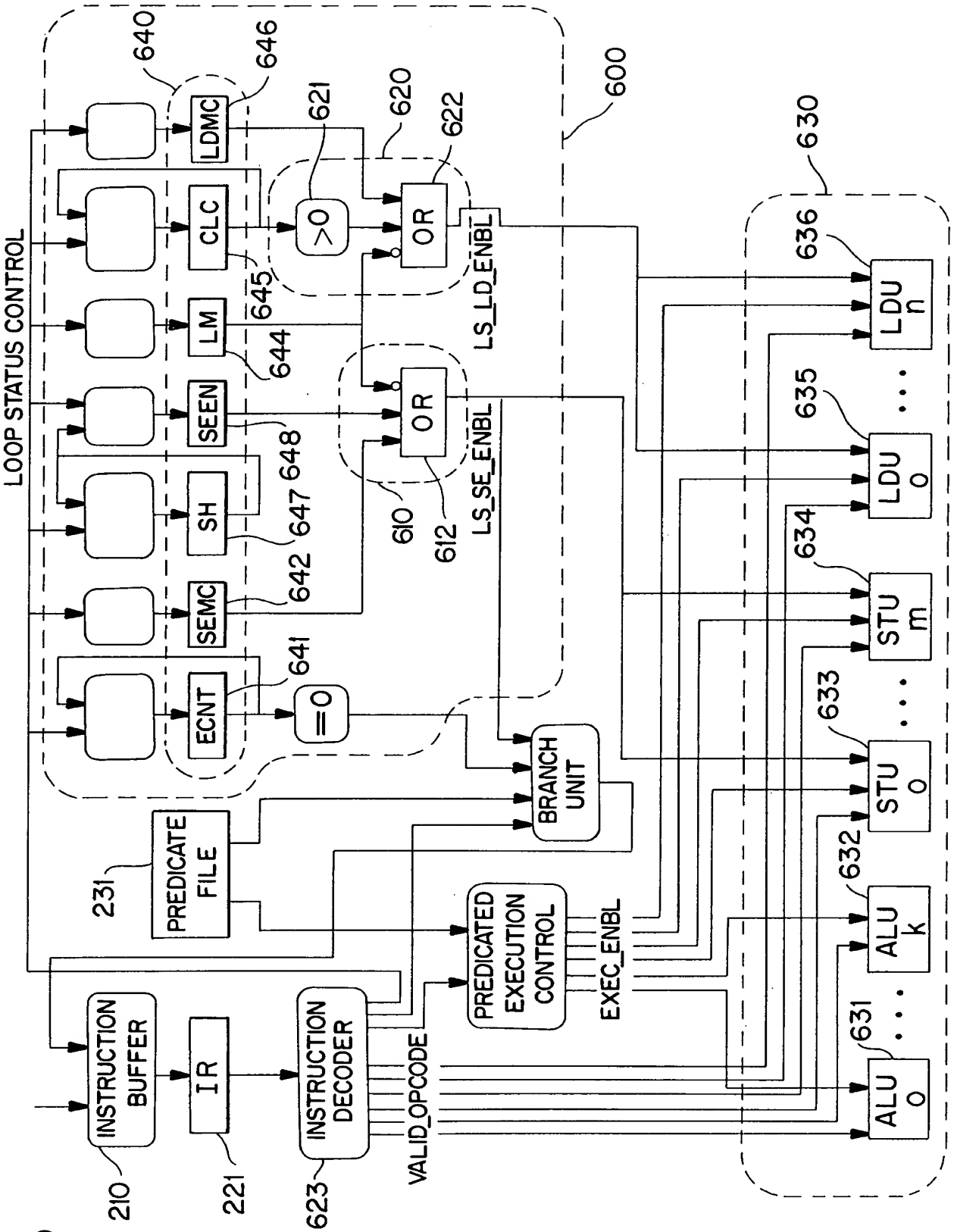

FIG. 5

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O.G. FIG.
CLASS
SUBCLASS

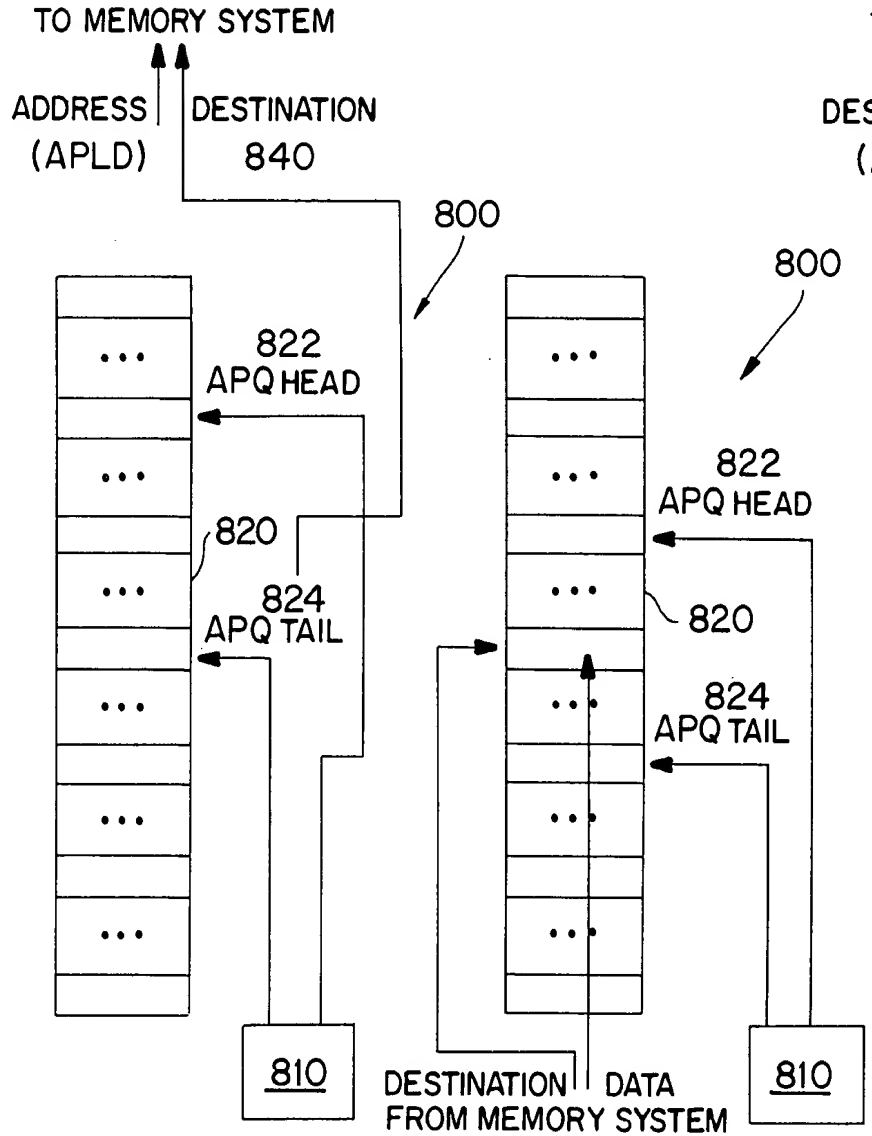

FIG. 6a

FIG. 6b

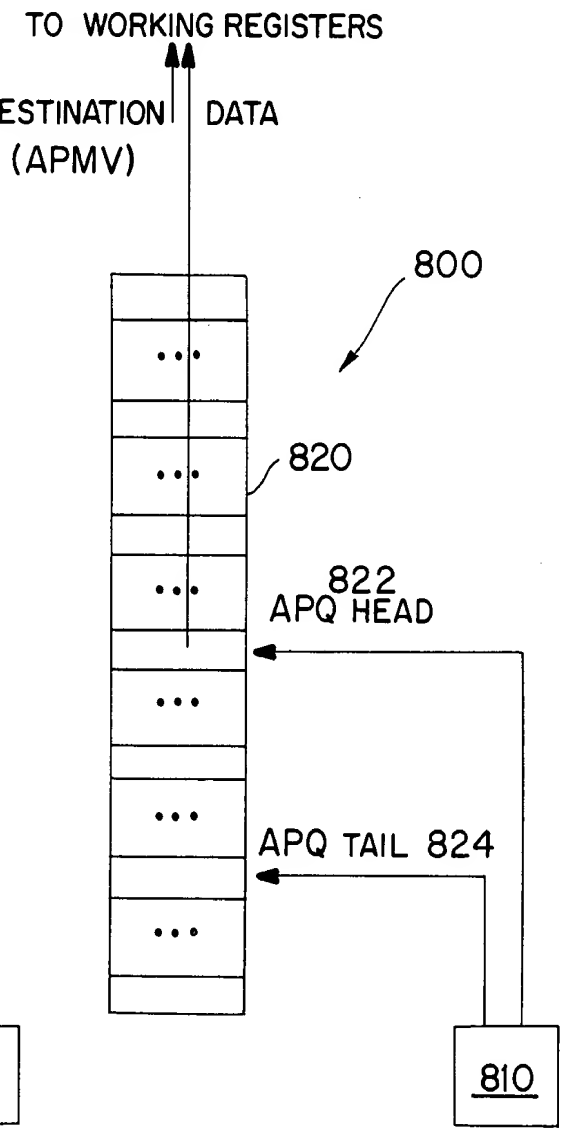

FIG. 6c

APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS

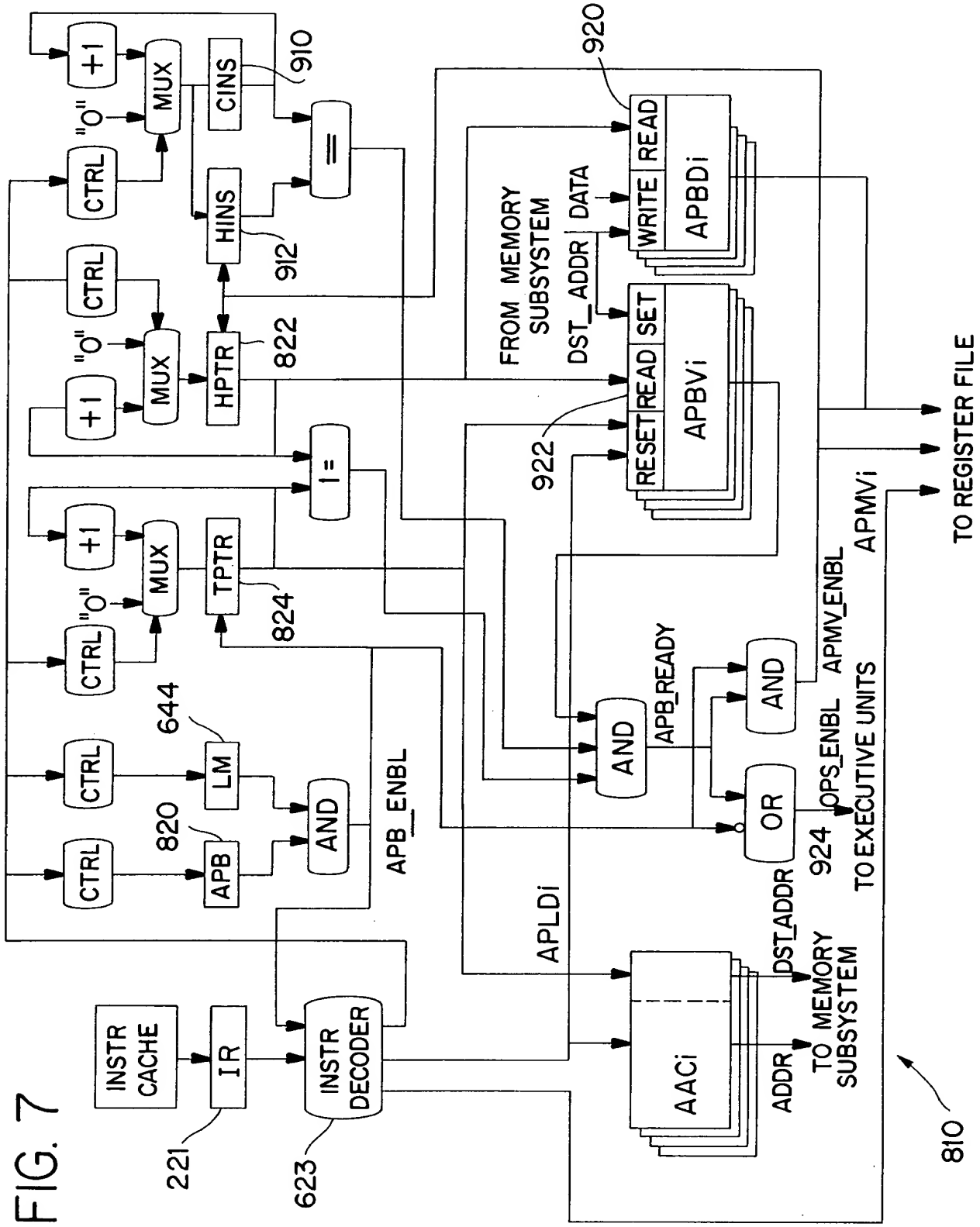

FIG. 7

APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS

PI	clc	ecnt	cldovl	tptr	hptr	cins	hins	apb_ready
1	4	1	0	0	0	0	0	0
				1	0	1	0	0
				2	0	2	0	0
				3	0	3	0	0
2	3	1	1	4	0	0	0	0
				5	0	1	0	0
				6	0	2	0	0
				7	0	3	0	0
3	2	1	2	8	0	0	0	1
				9	1	1	1	1
				10	2	2	2	1
				11	3	3	3	1
4	1	1	2	12	4	0	0	1
				13	5	1	1	1
				14	6	2	2	1
				15	7	3	3	1
5	0	1	2	16	8	0	0	1
				17	9	1	1	0
				18	9	2	1	0
				19	9	3	1	0
6	0	0	3	20	9	0	1	0
				20	9	1	1	1
				20	10	2	2	1
				20	11	3	3	1
7	0	0	2	20	12	0	0	1
				20	13	1	1	1
				20	14	2	2	1
				20	15	3	3	1
8	0	0	1	20	16	0	0	1
				20	17	1	1	1
				20	18	2	2	1
				20	19	3	3	1

FIG. 8A

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O.G. FIG.
CLASS
SUBCLASS

LOGICAL ITERATIONS				MEMORY DATA
0	1	2	3	
APLDa0->APB[0] APLDb0->APB[1] – –				
– – – –	APLDa1->APB[4] APLDb1->APB[5] – –			APB[0]=a0
APMVa0 APMVb0 FMUL –	– – – –	APLDa2->APB[8] APLDb2->APB[9] – –		APB[1]=b0 APB[4]=a1
– – – STOREc0	APMVa1 APMVb1 FMUL –	– – – –	APLDa3->APB[12] APLDb3->APB[13] – –	APB[5]=b1 APB[8]=a2
	– – – –	APMVa2 – – –	– – – –	APB[12]=a3 APB[9]=b2
	– – – STOREc1	– APMVb2 FMUL –	– – – –	APB[13]=b3
		– – – STOREc2	APMVa3 APMVb3 FMUL –	
			– – – STOREc3	

FIG 8B